







## Memorandum of Understanding

This Memorandum of Understanding (MoU) executed on 30<sup>th</sup> April, 2020, between ABES Engineering College, (herein after referred to as "ABES" or "University Partner" which expressions shall mean and include, unless repugnant to the context or meaning there of its successors-in-interest and permitted assigns) a State Private University Constituted vide the ABES, Campus -1, 19th KM Stone, NH 24, Ghaziabad, Uttar Pradesh 201009, as the First Party in this MoU AND

Truechip Solutions Pvt. Ltd. (here in after referred to "Truechip" which expressions shall mean and include, unless repugnant to the context or meaning there of its successors-ininterest and permitted assigns), a leading provider of Design and Verification solutions –
which help you, accelerate your design, lowering the cost and the risks associated with the
development of your ASIC, FPGA and SOC, with registered office located at D-25, 1st
Floor, Lajpat Nagar – II, New Delhi – 1100 24, INDIA, as a party of the second part
AND

Future Tech Wizards LLP (hereinafter referred to as "FutureWiz" which expressions shall mean and include, unless repugnant to the context or meaning there of its successors-in-interest and permitted assigns), is a leading provider of training (in VLSI) and design services, with its design center located at D-67, 1st floor, Sector 2, Noida, UP – 201301, INDIA, as a party of the third part.

Whereas ABES is a University, providing degree and diploma courses to students.

Whereas FutureWiz provides trainings (to students and corporates) and design services.

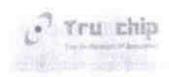
Whereas Truechip is engaged in providing Verification IP licenses and design services.

ABES (or ABES Engineering College), Truechip and FutureWiz would individually be termed as a "Party" and collectively be termed as "Parties".

Whereas Truechip, FutureWiz and ABES have agreed to collaborate together towards developing skilled man power and imparting VLSI training to students as per terms and conditions hereunder:

- Effective Date: This MoU shall be deemed to be in effect from 19th Feb, 2019 (hereinafter "Effective Date")
- As part of this MoU, Truechip & FutureWiz will offer VLS1 training to students doing engineering Dgree at ABES.
- The students will enrol for the VLSI Training and pay full applicable fees to FutureWiz as per agreed commercial proposal.





## 26. Further Assurance

The Parties shall do, execute or perform all such further deeds, documents, assurances, acts and things as may be reasonably required by the other Party to carry out the provisions of the Agreement into full force and effect.

IN WITNESS WHERE OF, the Parties hereto have duly executed these presents on the date, month and year mentioned below.

For Truechip Solutions Pvt. Ltd.

Name: Mr. Nitin Kishore

Designation (CEO)

For Futurewiz Academy LLP

Name Ms Artikishore

For ABES Engineering College

Name: Dr. Sanjay Kumar Singh Designation: HoD, Dept. of ECT.

Date: 30,04,2020

Place: Ghaziabad, INDIA: